

Local Address: (unlisted) , USA.

Permanent Address: (unlisted) , USA.

<b>Objective</b>	To acquire a challenging, full-time engineering position regional to Rochester, NY. Career interests include FPGA, ASIC, DSP, computer architecture, real-time/embedded software. Availability: presently employed with position.		
<b>Qualifications</b>	<ul style="list-style-type: none"> <li>• United States citizen, native-born.</li> <li>• Engineering and QA experience in USA, Hong Kong, and mainland China.</li> <li>• Master's Thesis work on frame buffer design for H.264/AVC video decoder with fidelity extensions.</li> </ul>		
<b>Education</b>	<p><a href="#">Rochester Institute of Technology</a>, Rochester, NY, USA.</p> <p>Dual Bachelor and Master of Science in Computer Engineering, upon thesis completion mid-2007. Thesis in progress, undergrad GPA: 3.61., graduate GPA: n/a.</p>		
<b>Relevant Course Work</b>	Comp. Architecture, Multiprocessing Interface, Electronics, Circuits	Digital Design, FPGA, VLSI Controls, Signal Processing	CS, Operating Systems Real-time Modeling
<b>Technical Skills (non-exhaustive)</b>	<p><i>Hardware Design:</i></p> <ul style="list-style-type: none"> <li>• Digital Logic: VHDL, ASIC, FPGA, VLSI, BIST, DFT, RTL, verification.</li> <li>• Synthesis: RTL, ASIC, FPGA, CPLD, memories, timing-analysis.</li> </ul> <p><i>Software Design:</i></p> <ul style="list-style-type: none"> <li>• Embedded: test design, filesystems, removable media, drivers, I/O, GUI, gcc &amp; Make.</li> <li>• Application: distributed, real-time, UNIX/cross-platform, Object-Oriented, UML.</li> <li>• Web: HTML, CSS, XML &amp; XSLT, JavaScript, Perl/CGI, PHP.</li> </ul> <p><i>Programming:</i> Assembly, C, C++ STL, Java, MPI, UNIX scripting, Perl, Python, Emacs Lisp.</p> <p><i>Various:</i> MATLAB/Simulink, L<sup>A</sup>T<sub>E</sub>X typesetting, Linux networking &amp; security, office computing.</p>		
<b>Communication Skills</b>	Assistant-teaching, public speaking, press release, and television star experience. Articulate spoken and written English abilities, especially with technical documentation. Survival-level workplace knowledge of American Sign Language.		
<b>Experience (non-exhaustive)</b>	<p><b><a href="#">Xelic, Inc.</a></b> <span style="float: right;"><i>Hardware Design Engineer</i></span>  Rochester, NY, USA <span style="float: right;"><b>11 September 2006 – present</b></span>  <span style="float: right;"><b>31 May 2005 – 31 September 2005</b></span>  <span style="float: right;"><b>23 August 2004 – 23 November 2004</b></span></p> <p><i>Test design</i> and verification of SONET &amp; OTN networking cores for ASIC and FPGA.  <i>RTL design</i> and synthesis for ASIC and FPGA. Board-level debugging.  <i>Researched</i>, designed, and documented many key EDA software solutions to improve turn-around time.  <i>Administration</i> and support of Linux-based simulation servers and engineering tools.</p> <p><b><a href="#">Concord Camera Corp., USA R&amp;D Dep.</a></b> <span style="float: right;"><i>Firmware/Software Engineer Co-op</i></span>  Hollywood, FL, USA <span style="float: right;"><b>27 May 2003 – 21 November 2003</b></span></p> <p><i>Development</i> and maintenance of digital camera firmware. Hardware diagnostics, filesystem validation, ISO/IEC/SD media specs. Electrical and protocol analysis of camera USB.  <i>Researched</i>, planned, and created many new software solutions and analytical tools for engineering departments across three offices. <i>International travel</i> to Hong Kong and China offices to assist with firmware QA and on-site implementation of new testing processes.  <i>Trained</i> coworkers (formally and informally) regarding new development tools and processes. Trained IT department in Linux operating system. Documentation. On-site and remote presentations and support.</p> <p><b><a href="#">Department of Computer Engineering</a></b> <span style="float: right;"><i>Teaching Assistant &amp; Mentoring Lab Coordinator</i></span>  RIT, Rochester, NY, USA <span style="float: right;"><b>August 2002 – May 2003</b></span></p> <p><i>Lab coordination for two departments:</i> Managed schedules, meetings, tutored &amp; mentored students.  <i>Teaching Assistant:</i> Robot debugging &amp; repair, grading, tutoring, demonstrations, shop work.</p>		
<b>Honors/Awards</b>	Eagle Scout Award, Golden Key membership, RIT Presidential Scholarship, RIT Reis Memorial Scholarship, LeChase Corp. Scholarship, National Merit Letter of Commendation, Rensselaer Math & Science Medal, Sigma Phi Epsilon Balanced Man Scholarship Finalist, <i>NY Expeditions</i> PBS television series.		